



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

*Handwritten:* #3/A, 43, DS

In re the Application of:

Takushi KIMURA et al.

Serial Number: 09/806,054

Group Art Unit: Unassigned

Filed: March 27, 2001

Examiner: Unassigned

For: PLL CIRCUIT

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
 Washington, D.C. 20231

May 25, 2001

Sir:

Prior to examination on the merits, please amend the above-identified application as follows:

**IN THE SPECIFICATION:**

Amend the specification as follows:

Please replace the paragraph beginning at page 11, line 24 with the following rewritten paragraph:

Further, in the above description, the operation stoppage state of the PLL circuit is judged by the presence/absence of an output signal of the frequency divider 4. However, the operation stoppage state of the PLL circuit may be judged by detecting whether or not the oscillation frequency fck of the voltage control oscillator 3 is a frequency of a predetermined value or more. In [the later]

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